

Attorney Docket No. 10113421

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Chi-Hui LIN

Filed: 12/11/2003

MAR 03 2005

Appl. No.: 10/733,626

Examiner:

Conf. No.: 8636

Art Unit: 2186

Title: STACKED GATE FLASH MEMORY DEVICE AND METHOD OF
FABRICATING THE SAME

Date: March 3, 2005

Mail Stop Amendment
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Responsive to the restriction requirement mailed on February 18, 2005, Applicant hereby elects the invention of Group II (claims 14-37) for examination, without traverse. This election is made without prejudice to Applicant's right to file divisional applications directed to the non-elected inventions. A prompt and favorable action on the merits of this application is now respectfully requested.

No fee is believed to be due in connection with this response. If, however, the Commissioner considers that a fee is due in connection with this response, authorization is made to charge any fee which may be required to Deposit Account No. 502447.

Certificate of Transmission

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent & Trademark Office to (703) 872-9306 on the date shown below:

Respectfully submitted,

Nelson A. Quintero
 (Name of Person Transmitting)

(Signature)

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March 3, 2005
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